





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Leonard Forbes et al.

Title:

TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF

FABRICATION AND USE

Attorney Docket No.: 303.324US2



BOX PATENT APPLICATION

Assistant Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- **DIVISIONAL** of prior Patent Application No. <u>08/903,452</u> (under 37 CFR § 1.53(b)) comprising: X
 - Specification (32 pgs, including claims numbered 1 through 35 and a 1 page Abstract). \mathbf{X}
 - Formal Drawing(s) (11 sheets). X
 - X Copy of signed Declaration and Power of Attorney (6 pgs) from prior application.
 - Incorporation by Reference: The entire disclosure of the prior application, from which a copy of the oath or ũ declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application ū and is hereby incorporated by reference therein.
- ГЦ Check in the amount of \$760.00 to pay the filing fee.
- Prior application is assigned of record to Micron Technology, Inc.
- Information Disclosure Statement (1 pgs), Form 1449 (9 pgs). References NOT enclosed, cited in prior m application.
- X= Preliminary Amendment (1 pgs).
- Xii Return postcard.

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The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	15 - 20 =	0	x 18 =	\$0.00
DEPENDENT CLAIMS	2 - 3 =	0	x 78 =	\$0.00
MULTIPLE DEPENDENT CLAIMS PR	\$0.00			
BASIC FEE	\$760.00			
TOTAL				\$760.00

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CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner	for Patents, Box Patent Application, Washington, D.C. 20231.

Signature: Mrs Kummen & Chris Hammond